Recruitment Information for Project Researcher (Fixed-Term Project Staff) in Iizuka Laboratory, Dept. of EEIS,School of Engineering, the University of Tokyo

1. Title and Hiring Numbers :	Project Researcher (Fixed-term project staff) 1 person
2. Term of Employment :	As soon as possible after October 1, 2026 to March 31, 2027
3. Renewal of Contract :	The contract is renewable on a fiscal year basis subject to progress of the work assigned, job performance, work record, and attitude, health conditions, budget status, the amount of the work at the end of the term. Contract is until November 30, 2030 at the longest.
4. Probation Period :	14 days from the date of appointment.
5. Place of Work :	Hongo campus, Asano area, The University of Tokyo (2-11-16 Yayoi, Bunkyo-ku, Tokyo) Stations : Subway Chiyoda Line Nezu Sta. 5min Namboku Line Todai-mae Sta. 10 min Marunouchi Line Hongo-sanchome Sta. 25 min *The scope of changes: Place of Work may be changed within Graduate School of Engineering subject to necessity of the business, etc.
6. Affiliation :	Iizuka Laboratory, Department of Electrical Engineering and Information Systems *Your affiliation may be changed subject to necessity of the business
7. Contents of Work Duties :	We are seeking a Specially Appointed Researcher to engage in research aimed at enhancing the performance and efficiency of advanced analog integrated circuits, such as analog-to-digital converters (ADCs), digital-to-analog converters (DACs), phase-locked loops (PLLs), and neuromorphic circuits. The work will also focus on simplifying analog IC design through the application of design automation technologies. For neuromorphic circuit technology, the objective is to devise appropriate artificial synapse and neuron circuit architectures and establish integrated implementation techniques to realize them, thereby contributing to the development of the academic foundation in this field. The researcher will collaborate with other team members to advance research projects related to the above topics. *The scope of change is as follows: Displacement, concurrent service, and secondment may be ordered.
8. Working Hours :	Discretionary work system for professional work applies and working hours will be deemed as 7 hours and 45 minutes per day.
9. Days off :	Saturdays, Sundays, Holidays, and the year-end and New Year holidays (Dec. 29th to Jan. 3rd)
10. Leave :	Annual Paid Leave, Special Leave, etc.
11. Wages, etc. :	To be determined by The University of Tokyo Regulations. An annual-based salary system is applied, and the monthly salary is about 400,000 yen to 1,200,000 yen, including performance and achievement allowances. The salary will be decided taking your qualifications, capacity, experience, etc. into account. Commuting Allowance is JPY 55,000 per month at maximum.
12. Insurance :	Eligible for the insurance from the Mutual Aid Association of MEXT (Ministry of Education, Culture, Sports, Science and Technology) and Employment Insurance.
13. Accident Compensation :	Work-related accidents and commuting accidents are covered by the Industrial Accident Compensation Insurance Act and the University of Tokyo's Teacher and Staff Act Non-Compliance Accident Compensation Regulations.
14. Qualification Requirements:	1) Those who have a doctoral degree, or those who are expected to acquire by the time of arrival. 2) Knowledge and experience on electronic and integrated circuits design. 3) Knowledge and experience in the use of automatic placement and routing software for integrated circuits. 4) Knowledge and experience in the design and evaluation of analog integrated circuit chips.
15. Documents to be Submitted :	1) The University of Tokyo Standard Resume (Download the resume from the website below.) https://www.u-tokyo.ac.jp/en/about/jobs.html 2) Publication list (categorized into original articles, review papers and others) 3) Overview of your research activities to date (1 to 2 pages in A4 or letter sheet) 4) Your future research plan (1 to 2 pages in A4 or letter sheet) 5) Contact information of two professional references (name, affiliation, phone number and e-mail address)
16. Submission Method :	Send PDF files of required materials by e-mail to the following address: tetsuya@ee.t.u-tokyo.ac.jp Tetsuya Iizuka, Professor The e-mail subject should be "Dept. of EEIS, Project Researcher Application"
17. Application Deadline :	Applications must arrive by 30th November 2026 (Mon.). Selection will be conducted and finished at any time even before the deadline. After screening of application materials, interviews of selected applicants will be conducted.
18. Contact :	2-11-16 Yayoi, Bunkyo-ku, Tokyo 113-0032, Japan Takeda Sentanchi Building 415 Iizuka Laboratory Responsible person:Tetsuya Iizuka TEL: 03-5841-8911 e-mail: tetsuya@ee.t.u-tokyo.ac.jp
19. Recruiter Name :	The University of Tokyo
20. The measures against second-hand smoking :	Smoking is not allowed on campus grounds. (only permitted in designated areas outside.)
21. Notice:	If you are personally in contract with foreign governmental bodies, corporations or universities, or you are in receipt of a large benefit (financial or any other form) from foreign governmental bodies during the period of your employment, the provisions of the Foreign Exchange and Foreign Trade Act (FEFTA) may prohibit or restrict the sharing of technology that are designated as controlled technology possibly making it difficult for you to fulfill your duties as an academic or administrative staff of the university as a result. Therefore, in such cases it is necessary to keep such contracts/benefits within the scope where it does not hinder the sharing of technologies necessary for your duties by the university.
22. Others :	Personal information is handled carefully according to the Privacy Policy of The University of Tokyo, and will be used only for this recruitment purposes. Submitted documents will not be returned. We welcome proactive application from women in accordance with “The University of Tokyo, Declaration for Promoting Gender Equality”.