

**AY2024 Admission (Effective 2023),
Department of Electrical Engineering
and Information Systems (EEIS),
Graduate School of Engineering,
The University of Tokyo**

Note: For the entrance examinations carried out in this academic year, examinations will be conducted online.

If the contents of this guide are changed due to the effects of COVID-19, we will announce it on our EEIS website. For this reason, please check the website once in a while.

AY2024 Admission (Effective 2023),
Department of Electrical Engineering and
Information Systems (EEIS),
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Entrance Examination Guide for the Master's Program

1 Examination Dates and General Information

Subject	Schedule	Place	What to bring	Notes
Preliminary selection	Preliminary selection may be made based on the "2024 Questionnaire, Department of Electrical Engineering and Information Systems" and "Transcripts and Grade Summary Sheets" of the applicants.			Please refer to 2(1)
English (TOEFL)	Deadline for submitting Wednesday, August 16	Submit your Appointment Number and examination date to the system.		For submission details, please refer to page 13
Online connection test	Thursday, August 17 10:00–14:00	The connection test will be conducted online		Please refer to 2(2)
Specialized subject (Electrical & Electronic Engineering and Information Engineering)	Monday, August 28 Test time 9:00–12:30	The written examination will be conducted online. A URL will be announced after admission cards are sent out.	The written examination will be conducted online. Please prepare the following equipments. *Personal computer *Web camera to take a side view *Camera or smartphone to scan your answer sheets *Broadband Internet connection	Please refer to 2(3)
Oral examination	Tuesday, August 29 8:00–18:00 or Wednesday, August 30 8:00–18:00 or Thursday, August 31 8:00–18:00 (tentative)	The oral examination will be conducted online. A URL will be announced after admission cards are sent out.	The oral examination will be conducted online. Please prepare the following equipments. *Personal computer *Web camera *Broadband Internet connection	Please refer to 2(4).

Notes:

※Examinees must take both written and oral examinations. Otherwise, the examination will be invalidated.

- * For the entrance examinations carried out in this academic year, the examinations will be conducted online.
- * This year, our written examinations will not include regular education subject (mathematics).
- * Please check the website of the Department of EEIS (<http://www.eeis.t.u-tokyo.ac.jp/en/>) for any updates to the online written examinations, which will be posted by Friday, August 25, 2023.
- * Details of the online oral examinations will be available on the EEIS webpage (<http://www.eeis.t.u-tokyo.ac.jp/en/>) after the admission cards are sent out.
- * On Thursday, August 17, 2023, a URL will be prepared for the connection check. Applicants must access this URL.
- * There will be no master's program examination on Friday, September 1, 2023.

2 Examination Subject

(1) Preliminary selection

Depending on the number of applicants, preliminary selection may be conducted based on the "2024 Questionnaire, Department of Electrical Engineering and Information Systems" and "Transcripts and Grade Summary Sheets" of the applicants. Applicants who pass the preliminary selection may take the specialized and oral examinations. We will not answer any inquiries regarding the criteria for the selection.

The results of the preliminary selection will be posted on the department webpage (<http://www.eeis.t.u-tokyo.ac.jp/en/index.html>) by Friday, August 11, 2023.

(2) Online Connection Test

The connection check prior to the online oral examination will be conducted on 17th August 2023. Please refer to the detailed procedure of the online oral examination posted on the EEIS webpage (<http://www.eeis.t.u-tokyo.ac.jp/en/>) after receiving the admission card, and then connect to the designated URL that will be sent to the applicants later. Please note that all applicants must participate in the connection test. The EEIS department will not consider any disadvantage caused by not participating.

(3) Specialized Subject (Electrical & Electronic Engineering and Information Engineering)

The test will cover the basic knowledge needed to conduct graduate-level research in electrical & electronic engineering and information engineering. The problems are written in English or Japanese. The answers can be given in English or Japanese.

Examinees are requested to select and answer two problems from the following. The time of examination is 150 minutes for the two problems in total.

Electromagnetism, Electrical Circuits, Information (I), Information (II), Solid-State Physics, Control and Electric Energy Engineering.

For more details and past examination problems, please refer to the department website (<http://www.eeis.t.u-tokyo.ac.jp/en/index.html>). Sample problems are also available on the department webpage. (They are not for sale in the print center of the School of Engineering).

The details of the online specialized-subject examination will be posted on the department webpage after the admission cards are sent out.

(4) Oral Examination

The oral examination is conducted online as a part of the application process. Please prepare the admission card and the necessary equipment for the online examination by referring to the details of the online test posted on the department website after the admission cards are sent out. The topics covered in the oral examination include the examinee's motivation for the application, research theme in the undergraduate school, basic knowledge of the field to which the examinee is applying, 2024 Questionnaire, Department of Electrical Engineering and Information Systems, and the results of the written examinations.

3 Supervisor Preference Survey

The supervisor preference survey will be conducted online at the time of application for the entrance examination. Applicants to the master's program are required to fill out the "Supervisor Preference Survey for Dept. of EEIS Entrance Exam. [Master Course]". The supervisor assignment is conducted in the fairest manner based on this list and the score of the examination. In some cases, an academic advisor not listed on the survey form may be assigned to the applicant. If you are a research student already belonging to the laboratories list, please enter your current host professor as the first choice.

Applicants who pass the examination will be notified of their supervisor assignment in a timely manner. Applicants who have passed the examination and decide not to enroll in the graduate

Entrance Examination Guide for the Master's Program

school should notify the Graduate School Team at the Office of Academic Affairs by submitting a document (Letter Declining Offer of Admission) as soon as possible.

4 Important Notice

Successful applicants will be placed in the course of their supervisor. Successful applicants may choose to start their program in October, provided that the applicants fulfill the entrance requirements designated by the School of Engineering. Entrance examination results may be utilized to improve future entrance examinations and education at the University of Tokyo. The Department of Electrical Engineering and Information Systems is not able to arrange visas for overseas applicants who wish to take the entrance examination.

5 2024 Questionnaire, Department of Electrical Engineering and Information Systems

Please access and fill out the online Questionnaire. For reference in the oral examination and the preliminary selection, please explain a research content that you desire to conduct in the Master's program. Additionally, please describe the theme of your graduation research and plans after graduation.

6 Transcripts and Grade Summary Sheets for Undergraduate Courses and Equivalents

Transcripts for undergraduate courses (or equivalents) are mandatory for all applicants including (expected) graduates of the Faculty of Engineering, the University of Tokyo. Please submit a "Grade Summary Sheet for Undergraduate Courses and Equivalents" along with your transcripts. Read the information on the reverse side of the sheet carefully, and fill in the sections correctly. These documents will be used as reference materials for preliminary selection, oral examination, and admission decision.

7 Security Export Control Regulations

The University of Tokyo has established the "The University of Tokyo Security Export Control Regulations" in accordance with Japan's "Foreign Exchange and Foreign Trade Act", and strictly screens prospective international students based on these regulations. Please note that international applicants who fall under any of the conditions set out in said regulations may not receive permission to enroll at the university or may have their research activities restricted. Details can be obtained from the following website: Office of Export Control <https://www.u-tokyo.ac.jp/adm/export-control/ja/rule.html> (Japanese only)

8 For applicants from Other Universities or with Other Study Backgrounds

The Department of Electrical Engineering and Information Systems aims to foster talents that can contribute to solving global challenges and creating novel values of technology through the inclusion of researchers with a wide variety of backgrounds such as electrical engineering, computer science, physics, material science, chemistry, energy/environmental engineering, control engineering, and space engineering. To encourage the participation of applicants with backgrounds other than electrical/electronic engineering and computer science, we revised the structure of Specialized subject examinations and reduced the required number of problems, from the entrance exams in the academic year of 2020. We are looking forward to working with students from different universities with various knowledge, experience, and research interests.

Entrance Examination Guide for the Doctoral Program Type A

1 Examination Dates and General Information

Subject	Schedule	Place	What to bring	Notes
English (TOEFL)	Deadline for submitting Wednesday, August 16	Submit your Appointment Number and examination date to the system.		For submission details, please refer to page 13
Online connection test	Thursday, August 17 10:00–14:00	The connection test will be conducted online		Please refer to 2(1-1)
Specialized subject (Electrical & Electronic Engineering and Information Engineering)	Monday, August 28 Test time 9:00–12:30	The written examination will be conducted online. A URL will be announced after admission cards are sent out.	The written examination will be conducted online. Please prepare the following equipments. *Personal computer *Web camera to take a side view *Camera or smartphone to scan your answer sheets *Broadband Internet connection	Please refer to 2(1-2)
Oral examination	Friday, September 1 8:00–18:00 (tentative)	The oral examination will be conducted online. A URL will be announced after admission cards are sent out.	The oral examination will be conducted online. Please prepare the following equipments. *Personal computer *Web camera *Broadband Internet connection	Please refer to 2(1-3). Please fill out the form of the "2024 Questionnaire, Department of Electrical Engineering and Information Systems".

Notes:

※Examinees must take both written and oral examinations. Otherwise, the examination will be invalidated.

- * This year, our written examinations will not include regular education subject (mathematics).
- * For applicants who are students from the University of Tokyo's Department of Electrical Engineering and Information Systems, Department of Advanced Energy, and Department of Information & Communication Engineering, oral examinations may be held before Friday, September 1 in some cases.
- * Check the website of the Department of EEIS (<http://www.eeis.t.u-tokyo.ac.jp/en/>) for any updates to the online written examinations, which will be posted by Friday, August 25, 2023.
- * Details of the online oral examinations will be available on the EEIS webpage (<http://www.eeis.t.u-tokyo.ac.jp/en/>) after the admission cards are sent out.
- * Applicants who have completed (or are expected to complete) a master's program at the University of Tokyo's Graduate School of Engineering are not required to take the English test.
- * Applicants who have graduated (or are expected to graduate) from a master's program in the University of Tokyo's Department of Electrical Engineering and Information Systems, Department of Electrical & Electronic Engineering, Department of Frontier Informatics, Department of Advanced Energy, or the Department of Information & Communication Engineering are not required to take the English or the specialized subject (Electrical & Electronic Engineering and Information Engineering) examination.
- * On Thursday, August 17, 2023, a URL will be prepared for the connection check. Applicants must access this URL.
- * Applicants who have graduated (or are expected to graduate) from a master's program in a department other than those listed above at the University of Tokyo may be exempt from the written examinations. Please inquire at the Office of the Department of Electrical Engineering and Information Systems (nyushi24@ee.t.u-tokyo.ac.jp) about examination exemption policies by Monday, June 12, 2023.

2 Overview of Examinations

(1) First-Stage Examination

The examination content for this year's doctoral program is as follows.

(1-1) Online Connection Test

The online connection test will be conducted prior to the online oral examination. Please refer to the detailed procedure of the online oral examination posted on the EEIS webpage (<http://www.eeis.t.u-tokyo.ac.jp/en/>).

www.eeis.t.u-tokyo.ac.jp/en/) after receiving the admission card, and then connect to the designated URL that will be sent to the applicants later. Please note that all applicants must participate in the connection test. The EEIS department will not consider any disadvantage caused by not participating.

(1-2) Specialized Subject (Electrical & Electronic Engineering and Information Engineering)

The test will cover basic knowledge needed to conduct graduate-level research in electrical & electronic engineering and information engineering. The problems are written on English or Japanese. The answers can be given in English or Japanese.

Examinees are requested to select and answer two problems from the following. The time of examination is 150 min for the two problems in total.

Electromagnetism, Electrical Circuits, Information (I), Information (II), Solid-State Physics, Control and Electric Energy Engineering.

For more details and past examination problems, refer to the department website (<http://www.eeis.t.u-tokyo.ac.jp/en/index.html>). Sample problems are also available on the department webpage. (They are not for sale in the print center of the School of Engineering).

The details of the online specialized-subject examination will be posted on the department webpage after the admission cards are sent out.

(1-3) Oral Examination

The oral examination is conducted online as a part of the application process. Please prepare the admission card and the necessary equipments for the online examination by referring to the details of the online test, which will be posted on the department website after the admission cards are sent out. Applicants will be asked to make a presentation about their master's thesis (even if it is still being written) or equivalent and their research plan in the PhD course. A Q&A session will follow the presentation, where some questions regarding the applicants' answers for the written examination may be asked as well. The length of the presentation will be instructed by interviewers in the examination (it is about 20 minutes). Applicants will be asked to upload the presentation slides to the designated URL that will be notified before the test.

(2) Second-Stage Examination

An oral examination, focusing on the individual candidate's master's thesis, will be conducted in February 2024 (tentative schedule) for applicants who pass the first-stage examination and are expected to finish their master's program. The actual date will be announced later. For applicants for October entrance, the first-stage examination is also regarded as the second-stage examination, so they will have no separate second-stage examination.

3 Required Documents

At the time of application, prepare (1) an acceptance letter from the preferred supervisor, (2) a "Ph.D. Program Research Plan", and (3) a reference letter from your current advisor or boss (if required, see (3)) to be submitted along with other necessary documents.

Entrance Examination Guide for the Doctoral Program Type A

(1) Acceptance Letter from the Preferred Supervisor

The acceptance letter must be in the format specified by the department. Applicants need to contact their preferred supervisor as early as possible before the application and ask the preferred supervisor to make an acceptance letter with the supervisor's signature. (Scanned electronic files or copies are acceptable.)

Applicants must submit it along with the application form. Acceptance letters in formats other than that specified are not acceptable.

(2) Ph.D. Program Research Plan

Research plans should outline the applicant's research methodology and vision in about 1,000 English words (or 2,000 Japanese characters) on single-sided, A4-sized paper (unstapled). Plans may be prepared in any format as long as the following points are outlined.

- Research goal (including the background and the current international status of the research)
- Details of the research (annual plans, etc.)
- Originality of the research

Applicants must discuss the details of the research to be conducted during their Ph.D. program with their preferred supervisor beforehand.

(3) Reference Letter from the Current Superior (if applicable)

The Doctoral course has classes and drills which require on-site attendance. Applicants who are currently employed and plan to retain their working status during the study program are required to submit a reference letter from their current superior stating that they will consider the schedule of the Ph. D. program after enrollment. Please also refer to the "Application Guidelines" for doctoral programs provided by the Graduate School of Engineering.

4 Important Notice

The application may be judged invalid if any of the required documents listed above is missing at the time of application. Entrance examination results may be utilized to improve future entrance examinations and education at the University of Tokyo. The Department of Electrical Engineering and Information Systems is not able to arrange visas for overseas applicants who wish to take the entrance examination.

5 Information about Supervisors

Applicants successfully admitted to the Ph.D. program are directly assigned to their preferred supervisors. Applicants need to contact their preferred supervisors as early as possible before submitting application documents.

6 Security Export Control Regulations

The University of Tokyo has established the "The University of Tokyo Security Export Control Regulations" in accordance with Japan's "Foreign Exchange and Foreign Trade Act", and strictly screens prospective international students based on these regulations. Please note that international applicants who fall under any of the conditions set out in said regulations may not receive permission to enroll at the university or may have their research activities restricted. Details can be obtained from the following website: Office of Export Control
<https://www.u-tokyo.ac.jp/adm/export-control/ja/rule.html> (Japanese only)

Entrance Examination Guide for the Doctoral Program Type B

(This content is subject to change depending on the future situation of the disease outbreak.)

1 Examination Dates and General Information

Subject	Schedule	Place	What to bring	Notes
English (TOEFL)	Deadline for submitting Tuesday, January 9	Submit your Appointment Number and examination date to the system.		For submission details, please refer to page 13
Online connection test	Tuesday, January 16 10:00–14:00	The connection test will be conducted online		Please refer to 2(1-1)
Specialized subject (Electrical & Electronic Engineering and Information Engineering)	Tuesday, January 23 13:30–15:30 (tentative)	The written examination will be conducted online. A URL will be announced after admission cards are sent out.	The written examination will be conducted online. Please prepare the following equipments. *Personal computer *Web camera to take a side view *Camera or smartphone to scan your answer sheets *Broadband Internet connection	Please refer to 2(1-2)
Oral examination	Wednesday, January 24 ~Thursday, January 25 8:00–18:00 (tentative)	The oral examination will be conducted online. A URL will be announced after admission cards are sent out.	The oral examination will be conducted online. Please prepare the following equipments. *Personal computer *Web camera *Broadband Internet connection	Please refer to 2(1-3). Please fill out the form of the "2024 Questionnaire, Department of Electrical Engineering and Information Systems".

Notes:

※Examinees must take both the written and oral examinations. Otherwise, the examination will be invalidated.

- * This year, our written examinations do not include regular education subject (mathematics).
- * Please check the website of the Department of EEIS (<http://www.eeis.t.u-tokyo.ac.jp/en/>) for any updates or changes to room assignments and the schedule, which will be posted by Monday, January 22, 2024.
- * For applicants who are students from the University of Tokyo's Department of Electrical Engineering and Information Systems, Department of Advanced Energy, and Department of Information & Communication Engineering, oral examinations may be held before Wednesday, January 24, 2024 in some cases.
- * Applicants who have completed (or are expected to complete) a master's program at the University of Tokyo's Graduate School of Engineering are not required to take the English test.
- * Applicants who have graduated (or are expected to graduate) from a master's program in the University of Tokyo's Department of Electrical Engineering and Information Systems, Department of Electrical & Electronic Engineering, Department of Frontier Informatics, Department of Advanced Energy, or the Department of Information & Communication Engineering are not required to take the English or specialized subject (Electrical & Electronic Engineering and Information Engineering) examination.
- * Applicants who have graduated (or are expected to graduate) from a master's program in a department other than those listed above at the University of Tokyo may be exempt from the written examinations. Please inquire at the Office of the Department of Electrical Engineering and Information Systems (nyushi24@ee.t.u-tokyo.ac.jp) about examination exemption policies by the end of October.
- * On Tuesday, January 16, 2024, a URL will be prepared for the connection check. Applicants must access this URL.
- * For this entrance examination, the written examination will be held at the local examination rooms on the University of Tokyo campus. Instead of on-site examination, the online entrance examination will be conducted for examinees who meet the requirements described in the document "Remarks for the local examination/Conditions for the online examination". Details of the online examination will be available on the EEIS webpage (<http://www.eeis.t.u-tokyo.ac.jp/en/>) after the admission cards are sent out.
- * Details of the online oral examinations will be available on the EEIS webpage (<http://www.eeis.t.u-tokyo.ac.jp/en/>) after the admission cards are sent out.

2 Overview of Examinations

(1) Online Connection Test

The online connection test will be conducted prior to the online oral examination. Please

refer to the detailed procedure of the online oral examinations posted on the EEIS webpage (<http://www.eeis.t.u-tokyo.ac.jp/en/>) after receiving the admission card, and then connect to the designated URL that will be sent to applicants later. Please note that all applicants must participate in the connection test. The EEIS department will not consider any disadvantage caused by not participating.

(2) Specialized Subject (Electrical & Electronic Engineering and Information Engineering)

The test will be given in a description-type exam format. It will cover basic knowledge required to conduct research in the field of your preferred supervisor (see page 14-19). The problems will be written in English. The answers can be given in English or Japanese.

The details of the online specialized-subject examination will be posted on the department webpage after the admission cards are sent out.

(3) Oral Examination

All applicants must take the oral examination, which is conducted online. Prepare the admission card and the necessary equipment for the online examination by referring to the department website after the admission cards are sent out. Applicants who have taken the written examination will be asked some questions regarding the specialized subject test, explained in (2). All applicants will be asked to make a presentation about their master's thesis (even if it is still being written) or equivalent. Applicants will also be asked to present their plans for their research after the acceptance to the Ph.D. course. The length of the presentation will be instructed by interviewers in the examination (it is about 20 minutes), followed by a Q&A session. Applicants will be asked to upload the presentation slides to the designated URL that will be notified before the test.

3 Required Documents

At the time of application, please prepare (1) an acceptance letter from the preferred supervisor, (2) a "Ph.D. Program Research Plan", and (3) a reference letter from your current advisor or boss (if required) to be submitted along with other necessary documents.

(1) Acceptance Letter from the Preferred Supervisor

The acceptance letter must be in the format specified by the department. Applicants need to contact their preferred supervisor as early as possible before the application and ask the preferred supervisor to make an acceptance letter with the supervisor's signature by the end of October, 2023. (Scanned electronic files or copies are acceptable.) Applicants must submit it along with the application form. Acceptance letters in formats other than that specified are not acceptable.

(2) Ph.D. Program Research Plan

Research plans should outline the applicant's research methodology and vision in about 1,000 English words (or 2,000 Japanese characters) on single-sided, A4-sized paper (unstapled). Plans may be prepared in any format as long as the following points are outlined.

- Research goal (including the background and the current international status of the research)
- Details of the research (annual plans, etc.)
- Originality of the research

Applicants must discuss the details of the research to be conducted during their Ph.D. program with their preferred supervisor beforehand.

Entrance Examination Guide for the Doctoral Program Type B

(3) Reference Letter from the Current Superior (if applicable)

The Doctoral course has classes and drills which require on-site attendance. Applicants who are currently employed and plan to retain their working status during the study program are required to submit a reference letter from their current superior stating that they will consider the schedule of the Ph. D. program after enrollment. Please also refer to the "Application Guidelines" for doctoral programs provided by the Graduate School of Engineering.

4 Important Notice

The application may be judged invalid if any of the required documents listed above is missing at the time of application. Entrance examination results may be utilized to improve the future entrance examinations and education at the University of Tokyo. The Department of Electrical Engineering and Information Systems is not able to arrange visas for overseas applicants who wish to take the entrance examination.

5 Security Export Control Regulations

The University of Tokyo has established the "The University of Tokyo Security Export Control Regulations" in accordance with Japan's "Foreign Exchange and Foreign Trade Act", and strictly screens prospective international students based on these regulations. Please note that international applicants who fall under any of the conditions set out in said regulations may not receive permission to enroll at the university or may have their research activities restricted. Details can be obtained from the following website: Office of Export Control
<https://www.u-tokyo.ac.jp/adm/export-control/ja/rule.html> (Japanese only)

Summary of the examinations [Master's and Doctoral (Type A) Programs]

1 Examination Dates	Monday, August 28 - Friday, September 1, 2023 (There will be no master's program examination on Friday, September 1, 2023.)
2 Methods	In the entrance examination of this year, the entrance examination is conducted online. Details of the online examination such as a URL for the access will be posted on the department webpage after the admission cards are sent out.
3 Important Notice for the Written Examination	(1) You will not be allowed to leave the examination room once the examination begins even if you finish it or want to abandon the examination. (2) Put your admission card on the table during the examination.
4 Important Notice for the Oral Examination	If you do not take the oral examination on the designated date during the examination, you will be considered to have abandoned the examination.
5 Second-Stage Examination for Doctoral Program	The second-stage examination will be conducted in early February 2024. Details of the examination will be announced on the department webpage.
6 Contact	7-3-1 Hongo, Bunkyo-ku, Tokyo 113-8656 The University of Tokyo Graduate School of Engineering Office of Department of Electrical Engineering and Information Systems TEL : +81-3-5841-6712 E-mail : nyushi24@ee.t.u-tokyo.ac.jp
7 Other	(1) Successful applicants will be posted on the notice board of the School of Engineering and its webpage (http://www.t.u-tokyo.ac.jp/soe/) at the beginning of September 2023. (2) After that, a letter of success in the examination will be sent to the successful applicants. We will not respond to any inquiries regarding the examination results by phone. (3) If there are any changes to your address or e-mail address after you have applied, please let us know as soon as possible.

Notice regarding the Foreign Language (English) Examination

You must submit the date of examination and Appointment Number of TOEFL iBT^{※ 1} or TOEFL iBT Home Edition^{※ 2}. If the Appointment Number is not provided, your entire examination might be invalidated.

※ 1 For the application for taking the TOEFL iBT, please check the ETS webpage.

※ 2 For information on the application, the use of the equipment, and the environment for taking the TOEFL iBT Home Edition, please check the ETS webpage.

It is necessary to submit the official score of a TOEFL test taken after the following date:

September 2021 (Master's program, Doctoral program type A), February 2022 (Doctoral program type B)

Deadline to apply for a TOEFL test: Before the application date for the entrance examination.

Deadline for submitting a TOEFL score: August 16, 2023 (Master's program, Doctoral program type A), January 9, 2024 (Doctoral program type B)

(1) How to submit a TOEFL score

- When you apply for the TOEFL iBT (or TOEFL iBT Home Edition), please request that the official score report be sent to the address below. In case you hope to send your past score, please ask ETS to send the official score report to the address below.

<Address for the official TOEFL score>

Scores sent without these codes will be invalidated.

DI (Designated Institution) code : "8596" (University of Tokyo Engineering)

Department code : "99" (Any Department Not Listed)

- The office of the Graduate School of Engineering will not be able to receive the score if you have not completed the above submission procedures. If you have completed the submission process, the office will be able to receive the score data online about two weeks after the test, provided that the test was successfully administered. Once the office has confirmed the data, your TOEFL score submission is completed.
- We use "Test Date Scores" (We do not use "My Best Scores").

(2) Important Notice

- If you plan to take the TOEFL test before the application deadline of the entrance examination and create the application documents via the "Entry form for the entrance examination", please enter your Appointment Number of the score report to be submitted. In case you hope to send your past score, please enter the Appointment Number of the score report to be submitted. Only the TOEFL score with the Appointment Number entered in the application will be valid.
- Submission of Test Taker Score Report is unnecessary.
- You cannot update your TOEFL score after you submit your application.
- There is a possibility that the test will be retaken or the score disclosure will be delayed due to the trouble of the organizer's machine, so please take the test well in advance.
- Please check the webpages of ETS and submit your score well in advance.
- You may also submit the score of the TOEFL ITP Plus for China. If you submit the score of the TOEFL ITP Plus for China, please submit a copy of the score notified to your Vericant account along with the application form. Additionally, please designate the Graduate School of Engineering (The University of Tokyo(Engineering)) as the data recipient from a page of "Send to Schools" in the student portal site of Vericant Co.Ltd.

List of the faculty

Power Engineering and Environmental Technology



Inomoto, Michiaki
Professor

Kashiwa

Nuclear fusion energy, Plasma science and engineering



Ohsaki, Hiroyuki
Professor

Kashiwa

Applied superconductivity, Electric machinery



Okada, Yoshitaka
Professor

Komaba

※

New energy, Solar cell technology,
Quantum nanostructures



Ono, Yasushi
Professor

Hongo, Kashiwa

※

Plasma physics and engineering,
Fusion energy development



Ono, Ryo
Professor

Hongo, Kashiwa

Plasma applications, medical, material
and aerospace applications, spectroscopic
measurements, reactive species



Kumada, Akiko
Professor

Hongo

High voltage engineering, Plasma physics,
Electric power apparatus



Sato, Masahiro
Associate Professor

Hongo

Dielectric and insulating materials, Materials informatics



Baba, Jumpei
Professor

Hongo, Kashiwa

Power Electronics, Smart Grid



Fujii, Takashi
Project Professor

Hongo

†

Laser sensing, Diagnostics of Electric Power Apparatus



Matsuhashi, Ryuji
Professor

Hongo

Energy economics, Environmental economics,
Measures to mitigate global warming

※ No assignment of students for this year.

† Students assigned to Fujii's laboratory are co-supervised with Prof. Kumada.

Space Engineering and Control Technology



Ohnishi, Wataru
Associate Professor

Hongo

Control engineering, Precision control, Learning control, Optimization, Mechatronics



Kubota, Takashi
Professor

Sagamihara ※

Space Robotics, AI, Image Recognition



Koseki, Takafumi
Professor

Hongo

Motion control, Rail—guided transport, Electric traction



Kobayashi, Daisuke
Associate Professor

Sagamihara

Space Electronics, IT Reliability & Sustainability, Environmental Hardening



Hashimoto, Tatsuaki
Professor

Sagamihara

Spacecraft Control, Image-based Navigation



Fukuda, Seisuke
Professor

Sagamihara

Spacecraft system, signal processing, microwave remote sensing



Fujimoto, Hiroshi
Professor

Kashiwa

Applied Control Engineering, Electric Vehicle and Airplane, Wireless Power Transfer

Nano Physics and Device Technology



Iwamoto, Satoshi
Professor

Komaba

Quantum Nanophotonics, Topological Photonics, Diamond Nanophotonics



Ohya, Shinobu
Associate Professor

Hongo

Spintronics, spin current, topological materials, oxide-based spintronics devices, quantum nano physics



Kobayashi, Masaki
Associate Professor

Hongo ‡

Spintronics, functional electronic materials, electronic structure, synchrotron radiation spectroscopy



Kobayashi, Masaharu
Associate Professor

Komaba

Next generation semiconductor transistor and memory technology, AI application nanoelectronic device



Shinya, Hikari
Project Associate Professor

Hongo †

Spintronics, First-principles Calculation, Computational Materials Design



Sugiyama, Masakazu
Professor


















Komaba

High-efficiency solar cells, Solar Fuel, Renewable energy system

※ No assignment of students for this year.

† Students assigned to Hikari Shinya's laboratory are co-supervised with Prof. Masaaki Tanaka

‡ Students assigned to Masaki Kobayashi's laboratory are co-supervised with Prof. Masaaki Tanaka.

 <p>Seki, Munetoshi Associate Professor</p> <p>Hongo ††</p> <p>Advanced energy conversion materials, Oxide electronics, Magnonics</p>	 <p>Someya, Takao Professor</p> <p>Hongo</p> <p>Organic and molecular electronics, Flexible devices and systems, Biological measurement, Wearable electronics</p>	 <p>Takagi, Shinichi Professor</p> <p>Hongo</p> <p>Semiconductor device, Nano device, MOSFET</p>
 <p>Takahashi, Takuji Professor</p> <p>Komaba</p> <p>Nano-probes, Nano-characterization, Surface physics</p>	 <p>Tekenaka, Mitsuru Professor</p> <p>Hongo</p> <p>Si photonics, electronic-photonic integrated circuit, optical computing</p>	 <p>Tanaka, Masaaki Professor</p> <p>Hongo</p> <p>Physics and devices using spin degrees of freedom as well as optical/transport properties, Spintronics materials and devices, Nanoscience and nanotechnology</p>
 <p>Tanemura, Takuo Associate Professor</p> <p>Hongo</p> <p>Photonic integrated circuit, nanophotonics, optical imaging</p>	 <p>Toshiyoshi, Hiroshi Professor</p> <p>Komaba</p> <p>Microelectromechanical Systems, MEMS</p>	 <p>Toprasertpong, Kasidit Lecture</p> <p>Hongo</p> <p>Semiconductor/Dielectric/Ferroelectric materials and devices, Optoelectronic devices, AI-accelerator devices</p>
 <p>Nakano, Yoshiaki Professor</p> <p>Hongo ※</p> <p>Optoelectronic devices, integrated photonics, optical energy</p>	 <p>Nomura, Masahiro Professor</p> <p>Komaba</p> <p>Energy harvesting, Integrated quantum electronics</p>	 <p>Hirakawa, Kazuhiko Professor</p> <p>Komaba ※</p> <p>Quantum semiconductor devices, nanoscience, terahertz dynamics</p>
 <p>Hiramoto, Toshiro Professor</p> <p>Komaba</p> <p>Integrated devices, Low power devices, Silicon power devices</p>	 <p>Matsuhisa, Naoji Associate Professor</p> <p>Komaba</p> <p>Soft Materials, Soft and Stretchable Electronics</p>	 <p>Yokota, Tomoyuki Associate Professor</p> <p>Hongo</p> <p>Organic electronics, Printed Electronics</p>
 <p>Lee, Sunghoon Lecture</p> <p>Hongo ※</p> <p>Organic electronics, Flexible/Soft Electronics</p>	 <p>Le Duc, Anh Associate Professor</p> <p>Hongo</p> <p>Topological Quantum Electronics, Oxide Electronics, Quantum Computing Science & Technology</p>	

※ No assignment of students for this year.

†† Students assigned to Seki's laboratory are co-supervised with Prof. Tabata.

Nano Bio Electronics and Complexity Engineering



Kohno, Takashi
Professor

Komaba

Neuromorphic Systems, Neuromimetic Systems,
Nervous system modeling



Kobayashi, Tetsuya J.
Professor

Komaba

Mathematical Biology, Systems Biology, Bioinformatics,
Biophysics



Sekino, Masaki
Professor

Hongo

Biomedical engineering, Functional neuroimaging,
Wearable devices



Tabata, Hitoshi
Professor

Hongo

Oxide electronics, Bio-electronics,
Quantum life science, Magnonics



Matsui, Hiroaki
Associate Professor

Hongo

※

Nano-optics, Nano-materials, biosensing, energy-
saving, thermal management, mechano-optics

Media, Intelligence and Computation Technology



Oishi, Takeshi
Associate Professor

Komaba

Computer Vision, Augmented/Mixed Reality



Saito, Daisuke
Associate Professor

Hongo

Speech Information Processing,
Multimedia Information Processing



Sato, Hiroyuki
Associate Professor

Hongo

Dielectric and insulating materials,
Materials informatics



Shimokawabe, Takashi
Associate Professor

Hongo, Kashiwa

High-performance Computing,
Accelerated Computing, Computational Mechanics



Hanawa, Toshihiro
Professor

Hongo, Kashiwa

High Performance Computing, Accelerator,
High-speed Interconnect



Minematsu, Nobuaki
Professor

Hongo

Speech Communication,
Computer-Aided Language Learning

※ No assignment of students for this year.

Ubiquitous Information Environment Technology



Ogawa, Takefumi
Associate Professor

Kashiwa

Augmented Reality, Human Computer Interaction,
Human Interface, Interactive Media



Kamezaki, Mitsuhiro
Project Professor

Hongo

†

Robotics, Human-symbiotic smart mobility,
Functional material-based smart device



Kawahara, Yoshihiro
Professor

Hongo

Digital Fabrication, Sensing, Internet of Things



Kudoh, Tomohiro
Professor

Hongo, Kashiwa

Information system infrastructure,
IT resource management



Nakamura, Ryo
Associate Professor

Hongo

Network Architecture, System Software



Nakayama, Masaya
Associate Professor

Hongo, Kashiwa

Wide area distributed processing technology



Narusue, Yoshiaki
Associate Professor

Hongo

Wireless Engineering, Cyber-Physical System,
Edge AI



Morikawa, Hiroyuki
Professor

Hongo

Beyond 5G/6G, Cloud Robotics, Digital Society
Design



Yatani, Koji
Associate Professor

Hongo

Human-Computer Interaction,
AI/IoT applications, Usable security

† Students assigned to Mitsuhiro Kamezaki's laboratory are co-supervised with Prof. Yoshihiro Kawahara.

LSI System Engineering



Iizuka, Tetsuya
Associate Professor

Hongo

Analog/Digital Mixed-Signal Advanced Integrated Systems



Ikeda, Makoto
Professor

Hongo

Hardware Security and System LSI, Smart Image Sensor Systems



Kuroda, Tadahiro
Professor

Hongo ※

Integrated Circuits, 3D Integration, AI



Kosuge, Atsutake
Lecture

Hongo †

Low-power Circuits, AI processor, Computational Sensing



Takamiya, Makoto
Professor

Komaba

Integrated Power Management, Power Electronics



Takeuchi, Ken
Professor

Hongo

Data-Centric Computing, Computation in Memory, AI chip, Approximate Computing, Co-design of Hardware & Software



Nakane, Ryosho
Project Associate Professor

Hongo ※

Spintronics, semiconductor devices, machine-learning devices



Hamada, Mototsugu
Project Professor

Hongo ※

Lowpower Circuits, Wireless Communication Systems, 3D Integration



Mita, Yoshio
Associate Professor

Hongo

MEMS, Intelligent Semiconductor Microdevices

Photonics and Wireless Engineering



Ozeki, Yasuyuki
Professor

Komaba

Biomedical imaging, pulsed lasers, quantum optics



Set, Sze Yun
Project Associate Professor

Hongo ※

Ultrafast Pulsed Laser, 3D Imaging, LIDAR



Natsuaki, Ryo
Associate Professor

Hongo

Measurement Engineering, Synthetic Aperture Radar, Remote Sensing



Hirose, Akira
Professor

Hongo

Neural networks (AI data processing), Wireless electronics (Imaging, Sensing)



Yamashita, Shinji
Professor

Hongo

Fiber Photonics, Fiber lasers, Fiber sensing

※ No assignment of students for this year.

† Students assigned to Atsutake Kosuge's laboratory are co-supervised with Prof. Makoto Ikeda.

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